1	•			,			
adj instruction\$1) and 128-bit USPAT; USCCR; FPRS, EPC; JPO; DERWENT; IBM_TDB 1 (test\$3 near memory) and (initiat\$3 adj startup) and determin\$3 and generat\$3 and pass\$3 114 1 (test\$3 near memory) and (CPU or Central Processing Unit) and (64-bit adj MMX) with register\$1 1 (test\$3 near memory) and (CPU or Central Processing Unit) and (64-bit adj MMX) with register\$1 1 (test\$3 near memory) and (CPU or Central Processing Unit) and (64-bit adj MMX) with register\$1 1 (test\$3 near memory) and (64-bit uSPAT uSPAT) uSOAT; USOAR; EPC; JPO; DERWENT; IBM_TDB 1 (test\$3 near memory) and (64-bit uSPAT; USOAR; EPC; JPO; DERWENT; IBM_TDB 1 (test\$3 near memory) and (64-bit uSPAT; USOAR; EPC; JPO; DERWENT; IBM_TDB 1 (test\$3 near memory) and (64-bit uSPAT; USOAR; EPC; JPO; DERWENT; IBM_TDB 1 (test\$3 near memory) and (64-bit uSPAT; USOAR; EPC; JPO; JPO; DERWENT; IBM_TDB 1 (test\$3 near memory) and (54-bit uSPAT; USOAR; EPC; JPO; JPO; DERWENT; IBM_TDB 1 (test\$3 near memory) and (54-bit uSPAT; USOAR; EPC; JPO; JPO; DERWENT; IBM_TDB 1 (test\$3 near memory) and (64-bit uSPAT; USPAT; USOAR; EPC; JPO; JPO; DERWENT; IBM_TDB 1 (test\$3 near memory) and (54-bit uSPAT) uSPAT; USP	111)	1		USPAT; USOCR; FPRS; EPO; JPO; DERWENT;	OR	ON	2007/03/21 11:06
adj startup) and determin\$3 and generat\$3 and pass\$3 (test\$3 near memory) and (CPU or Central Processing Unit) and (64-bit adj MMX) with register\$1 1 (test\$3 near memory) and (64-bit adj MMX) with register\$1 1 (test\$3 near memory) and (64-bit adj MMX) with register\$1 1 (test\$3 near memory) and (64-bit adj MMX) with register\$1 1 (test\$3 near memory) and (64-bit adj MMX) with register\$1 1 (test\$3 near memory) and (64-bit adj MMX) with register\$1 1 (test\$3 near memory) and (64-bit adj MMX) with register\$1 1 (test\$3 near memory) and (64-bit adj MMX) with register\$1 1 (test\$3 near memory) and (64-bit uspAT; usoCR; FPRS; EPO; JPO; DERWENT; IBM_TDB USPAT; usoCR; FPRS; EPO; JPO; DRY	L12	1		USPAT; USOCR; FPRS; EPO; JPO; DERWENT;	OR	ON	2007/03/21 13:13
Central Processing Unit) and (64-bit adj MMX) with register\$1 1 (test\$3 near memory) and (64-bit adj MMX) with register\$1 1 (test\$3 near memory) and (CPU or Central Processing Unit) and (64-bit adj MMX) with register\$1 1 (test\$3 near memory) and (CPU or Central Processing Unit) and (64-bit adj MMX) with register\$1 1 (test\$3 near memory) and (64-bit adj MMX) with register\$1 1 (test\$3 near memory) and (64-bit adj MMX) with register\$1 1 (test\$3 near memory) and (64-bit adj MMX) with register\$1 1 (test\$3 near memory) and (64-bit adj MMX) with register\$1 1 (test\$3 near memory) and (64-bit uspAT; uspAT; uspCR; FPRS; EPO; JPO; DERWENT; IBM_TDB 1 (18	113	4	adj startup) and determin\$3 and		OR .	ON	2007/03/21 14:22
adj MMX) with register\$1 1 (test\$3 near memory) and (CPU or Central Processing Unit) and (64-bit adj MMX) with register\$1 1 (test\$3 near memory) and (64-bit adj MMX) with register\$1 1 (test\$3 near memory) and (64-bit adj MMX) with register\$1 1 (test\$3 near memory) and (64-bit adj MMX) with register\$1 1 (test\$3 near memory) and (64-bit adj MMX) with register\$1 1 (test\$3 near memory) and (64-bit uspAT; uspAT; uspAT; uspAT; uspAT; uspAT; uspAT; uspAT; uspAT 1 (test\$3 near memory) and (64-bit uspAT; uspAT; uspAT; uspAT 1 (test\$3 near memory) and uspAT;	L14	1	Central Processing Unit) and (64-bit		OR	ON	2007/03/21 14:00
Central Processing Unit) and (64-bit adj MMX) with register\$1 USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB US-PGPUB; USPAT US-PGPUB; USPAT US-PGPUB; USPAT US-PGPUB; OR ON 2007/03/21 14:02 US-PGPUB; USPAT US-PGPUB; OR ON 2007/03/21 14:02 US-PGPUB; USPAT US-PGPUB; OR ON 2007/03/21 14:02 US-PGPUB; USPAT US-PGPUB; OR ON 2007/03/21 14:04 US-PGPUB; USPAT US-PGPUB; OR ON 2007/03/21 14:02 US-PGPUB; USPAT US-PGPUB; OR ON 2007/03/21 14:02 US-PGPUB; USPAT US-PGPUB; OR ON 2007/03/21 14:02 US-PGPUB; OR ON 2007/03/21 14:05 USPAT US-PGPUB; OR ON 2007/03/21 14:05 USPAT US-PGPUB; OR ON 2007/03/21 14:06 USPAT US-PGPUB; OR ON 2007/03/21 14:06 USPAT	L15	1			OR	ON	-2007/03/21 13:59
Adj MMX) with register\$1	116	1	Central Processing Unit) and (64-bit	USPAT; USOCR; FPRS; EPO; JPO; DERWENT;	OR	ON	2007/03/21 14:00
USPAT USPAT USPAT USPAT USPAT USPAT USPAT USPAT OR ON 2007/03/21 14:02 USPAT USPAT OR ON 2007/03/21 14:02 USPAT USPAT USPAT USPAT USPAT OR ON 2007/03/21 14:04 USPAT USPAT USPAT USPAT USPAT USPAT USPAT USPAT USPAT OR ON 2007/03/21 14:06 USPAT USPAT USPAT USPAT USPAT USPAT USPAT USPAT OR ON 2007/03/21 14:06 USPAT USPAT USPAT USPAT USPAT	(17)	1		USPAT; USOCR; FPRS; EPO; JPO; DERWENT;	OR .	ON	2007/03/21 14:00
USPAT USPAT USPAT USPAT USPAT USPAT USPAT USPAT ON 2007/03/21 14:04 USPAT USPAT USPAT USPAT USPAT ON 2007/03/21 14:02 USPAT USPAT USPAT USPAT USPAT ON 2007/03/21 14:02 USPAT USPAT USPAT USPAT ON 2007/03/21 14:06 USPAT USPAT USPAT USPAT ON 2007/03/21 14:29 ON 2007/03/21 14:29	L18	25	(64-bit adj MMX) with register\$1		OR	ON	2007/03/21 14:02
1 2 and 20 USPAT US-PGPUB; OR ON 2007/03/21 14:02 US-PGPUB; OR ON 2007/03/21 14:02 USPAT US-PGPUB; OR ON 2007/03/21 14:06 USPAT US-PGPUB; OR ON 2007/03/21 14:29 or maintaining or resetting) adj US-PGPUB; USPAT	L19	1	2 and 18		OR	ON	2007/03/21 14:02
USPAT ON 2007/03/21 14:06 USPAT ON 2007/03/21 14:29 Or maintaining or resetting) adj	120	27	(64-bit adj MMX)		OR	ON	2007/03/21 14:04
1 2 and 20 US-PGPUB; OR ON 2007/03/21 14:06 USPAT 15 (test\$3 near memory) and (setting or maintaining or resetting) adj US-PGPUB; USPAT OR ON 2007/03/21 14:29 USPAT	(21)	1	2 and 20		OR	ON	2007/03/21 14:02
15 (test\$3 near memory) and (setting or maintaining or resetting) adj US-PGPUB; OR ON 2007/03/21 14:29	(L22)	1	2 and 20		OR	ON	2007/03/21 14:06
	123	15	or maintaining or resetting) adj	US-PGPUB;	OR	ON	2007/03/21 14:29

L24 1	(test\$3 near memory) and (setting and maintaining and resetting) adj (gate A20)	US-PGPUB; USPAT	OR	ON	2007/03/21 14:28
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Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	430	(one or "1") adj (mbyte or megabyte) same (increment\$ or iteration or portion)\	US-PGPUB; USPAT	OR	ON	2007/03/21 09:17
L2	11480	test\$3 near memory	US-PGPUB; USPAT	OR	ON	2007/03/21 09:19
L3	144831	increment\$3 and generat\$3 and repeat\$3	US-PGPUB; USPAT	OR	ON	2007/03/21 09:21
[4]	16	1 and 2	US-PGPUB; USPAT	OR	ON	2007/03/21 09:21
L5	7	3 and 4	US-PGPUB; USPAT	OR	ON	2007/03/21 09:21

Ref	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
L1	430	(one or "1") adj (mbyte or megabyte) same (increment\$ or iteration or portion)\	US-PGPUB; USPAT	OR	ON	2007/03/21.09:17
L2*	11480	test\$3 near memory	US-PGPUB; USPAT	OR	ON	2007/03/21 14:22
L3	144831	increment\$3 and generat\$3 and repeat\$3	US-PGPUB; USPAT	OR	ON	2007/03/21 09:21
LA	16	1 and 2	US-PGPUB; USPAT	OR	ON	2007/03/21 09:21
15	7	3 and 4	US-PGPUB; USPAT	OR	ON	2007/03/21 09:21
L6	1	(test\$3 near memory) and (MOVNTDQ or MOV) with instruction and 128-bit	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/21 10:29
L7	0	(test\$3 near memory) and MOV with instruction and 128-bit	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/21 10:33
L8	0	(test\$3 near memory) and MOV and 128-bit	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/21 10:33
19	4	(test\$3 near memory) and (move with instruction) and 128-bit	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/21 11:05
L10	0	(test\$3 near memory) and (MOV with instruction) and 128-bit	US-PGPUB; USPAT; USOCR; FPRS; EPO; JPO; DERWENT; IBM_TDB	OR	ON	2007/03/21 10:35